

References

1. Massie, H. L.; US Patent 5822166, ‘*DC power bus voltage transient suppressor circuit*’, filed: December 5, 1996
2. Nair, R.; US Patent 6084385, ‘*System and method for multi-mode low power voltage regulator*’, filed: June 22, 1999
3. Nair, R.; US Patent 6081105, ‘*Multi-mode low power voltage regulator*’, filed: March 08, 1999
4. Massie, H. L.; US Patent 6285175, ‘*DC-to-DC converter with transient suppression*’, filed: May 2, 2000
5. Thomas, T. P. et al.; US Patent 6538497, ‘*On-chip power supply boost for voltage droop reduction*’, filed: March 27, 2001
6. Barnes, J. A. et al.; US Patent 6677736, ‘*Energy recovery system for droop compensation circuitry*’, filed: Sept. 28, 2001
7. R. Mahajan, Raj Nair et al., ‘*Emerging Directions in Packaging Technology*’, Intel Technology Journal, Volume 6, Issue 2, May 2002 http://www.intel.com/technology/itj/2002/volume06issue02/art07_emergingdirections/p01_abstract.htm
8. Nair, R. and Tang, J.; ‘*Decoupled Signal-Power Substrate Architecture*’, US Patent pending, USPTO appl. no. 10/335,026
9. Ostrom, K. A.; US Patent 6661212, ‘*Wideband regulator with fast transient suppression circuitry*’, filed: January 8, 2003
10. Orenchak, George; ‘*Ferrites – “The most important properties”*”, 1994 Soft Ferrites Users’ Conference